

4A, 50V - 1000V Standard Bridge Rectifier

FEATURES

- AEC-Q101 qualified available
- Glass passivated chip junction
- Ideal for printed circuit board
- High case dielectric strength
- Typical IR less than 0.1µA
- UL Recognized File # E-326243
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

APPLICATIONS

- Switching mode power supply (SMPS)
- Adapters
- Lighting application

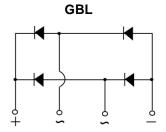
MECHANICAL DATA

- · Case: GBL
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: As marked
- Weight: 2.00g (approximately)

KEY PARAMETERS					
PARAMETER VALUE UNI					
I _F	4	Α			
V_{RRM}	50 - 1000	V			
I _{FSM}	150	Α			
T_{JMAX}	150	°C			
Package	GBL				
Configuration	Quad				







ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise noted)										
PARAMETER		SYMBOL	GBL 005	GBL 01	GBL 02	GBL 04	GBL 06	GBL 08	GBL 10	UNIT
Marking code on the o	levice		GBL005	GBL01	GBL02	GBL04	GBL06	GBL08	GBL10	
Repetitive peak revers	se voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Reverse voltage, total	rms value	V _{R(RMS)}	35	70	140	280	420	560	700	V
Forward current $ T_{C} = 50^{\circ}C $ $T_{A} = 40^{\circ}C $			4						Α	
		l _F	3						Α	
Surge peak forward co 8.3ms single half sine superimposed on rate	-wave	I _{FSM}	I _{FSM} 150			А				
Rating for fusing (t<8.5	3ms)	l ² t	l ² t 93			A ² s				
Junction temperature		Τ _J	- 55 to +150			°C				
Storage temperature		T _{STG}	- 55 to +150				°C			



THERMAL PERFORMANCE							
PARAMETER	SYMBOL	TYP	UNIT				
Junction-to-lead thermal resistance	R _{OJL}	13	°C/W				
Junction-to-ambient thermal resistance	R _{OJA}	32	°C/W				
Junction-to-case thermal resistance	R _{eJC}	8	°C/W				

ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise noted)							
PARAMETER		CONDITIONS	SYMBOL	TYP	MAX	UNIT	
Forward voltage per diode ⁽¹⁾		I _F = 2A, T _J = 25°C	V _F	-	1.0	V	
		$I_F = 4A, T_J = 25^{\circ}C$		-	1.1	V	
Reverse current @ rated V _R per diode ⁽²⁾		T _J = 25°C		-	5	μA	
		T _J = 125°C	- I _R	-	500	μΑ	
Junction capacitance per diode	GBL005 GBL01 GBL02 GBL04	$1MHz, V_R = 4.0V$	C₃	95	-	pF	
	GBL06 GBL08 GBL10		J	40	-	pF	

Notes:

- 1. Pulse test with PW = 0.3ms
- 2. Pulse test with PW = 30ms

ORDERING INFORMATION						
ORDERING CODE ⁽¹⁾⁽²⁾ PACKAGE PACKING						
GBLx	GBL	25 / Tube				
GBLxH	GBL	25 / Tube				

Notes:

- 1. "x" defines voltage from 50V(GBL005) to 1000V(GBL10)
- 2. "H" means AEC-Q101 qualified



CHARACTERISTICS CURVES

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

Fig.1 Forward Current Derating Curve

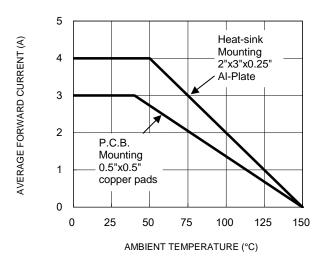


Fig.3 Typical Reverse Characteristics

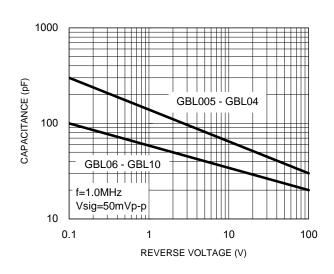
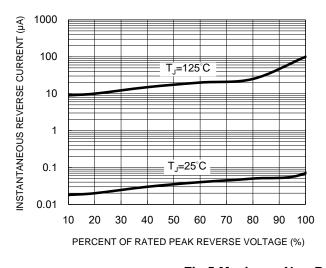


Fig.2 Typical Junction Capacitance

Fig.4 Typical Forward Characteristics



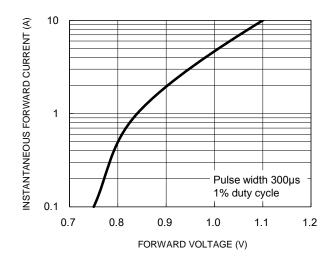
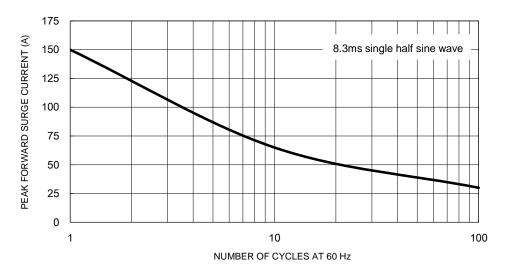


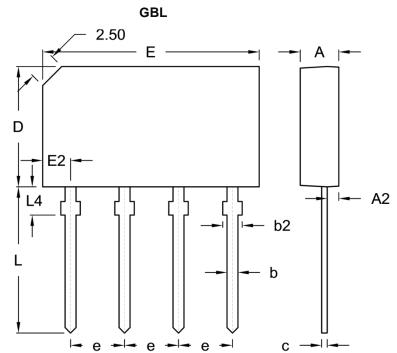
Fig.5 Maximum Non-Repetitive Forward Surge Current





Taiwan Semiconductor

PACKAGE OUTLINE DIMENSIONS



DIM. Unit (mm		(mm)	Unit (inch)		
Dilvi.	Min. Max.		Min.	Max.	
Α	3.30	3.70	0.130	0.146	
A2	0.80	1.20	0.031	0.047	
b	0.90	1.10	0.035	0.043	
b2	1.30	2.00	0.051	0.079	
С	0.40	0.60	0.016	0.024	
D	10.70	11.30	0.421	0.445	
E	19.70	20.30	0.776	0.799	
E2	2.30	2.70	0.091	0.106	
е	4.80	5.20	0.189	0.205	
L	13.00	14.00	0.512	0.551	
L4	2.30	2.70	0.091	0.106	

MARKING DIAGRAM

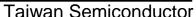


P/N = Marking Code

G = Green Compound

YWW = Date Code

F = Factory Code





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